

Ultra High Precision Bulk Metal[®] Foil Technology 4 Resistor Surface Mount <u>Hermetic Network</u> with <u>0.5 ppm/°C</u> TCR Tracking and <u>0.005 %</u> Tolerance Match



INTRODUCTION

Vishay model SMNH networks incorporate all the performance features of Vishay Bulk Metal[®] Foil technology in a product ready for surface mounting. The 8 pin gull wing side brazed DIP is the smallest ceramic package. Ceramic has the advantage of electrical isolation on the underside and high heat dissipation capability.

Resistors in network form, around the operational amplifier for example, are called upon to track at changing ambient temperature, to hold ratio under power and to force the ratio changes over a period of time to be very low.

The hermeticity, the location of the chips within the package and the "heat-sink effect" of the ceramic package itself help preserve uniform conditions inside it. The Bulk Metal Foil Vishay technology advantage in such a construction assures remarkable performance due to the following factors:

- fundamentally low TCR
- very small drift with load over time
- common behavior: all drifts move in the same direction with temperature, load and time
- TCR and tolerance match
- excellent tracking

The major highlights are emphasized by an excellent load life and shelf life ratio stability.

Vishay hermetic resistor networks are based on fabrication from a standing inventory of packages and resistor chips. This permits quick delivery of prototypes since there are no masks to design or trial processings to be made. Further, it allows any combination of values, tolerances and circuits. There are normally no engineering or setup charges, and no minimum quantities are required. Delivery can be in two weeks. (See network express prototype service.)

The sequence of fabrication includes selection of chips, die attachment, wirebonding, value trimming, and hermetic sealing. The finished product provides the stability associated with Foil resistors in a hermetically sealed package.

Hermetic sealing of Vishay's networks enhances their already inherently stable environmental performance. The result is improved load life stability and better performance during high temperature and moisture exposure.

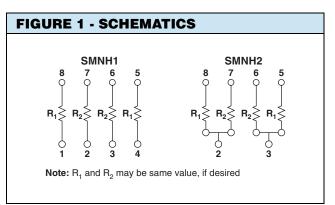
FEATURES

 Temperature coefficient of resistance (TCR): absolute: ± 2 ppm/°C typical (- 55 °C to + 125 °C, + 25 °C ref.) tracking: ± 0.5 ppm/°C typical



COMPLIANT

- Resistance range: 5 Ω to 33 k Ω
- Vishay Foil resistors are not restricted to standard values; specific "as required" values can be supplied at no extra cost or delivery (e.g. 1K2345 vs. 1K)
- Power rating: at 70 °C Entire package: 0.4 W Each resistor: 0.1 W
- Resistance tolerance match: ± 0.005 %
- Load life stability per resistor: 0.005 % (0.1 W at 70 °C, 1000 h)
- Load life stability ratio: 0.005 % (0.1 W at 70 °C)
- Shelf life stability per resistor: 0.0002 % (2 ppm)
- Shelf life stability ratio: 0.0001 % (1 ppm)
- Electrostatic discharge (ESD) up to 25 000 V
- Short time overload \leq 0.002 % (20 ppm)
- Non-inductive, non-capacitive design
- Rise time: 1 ns effectively no ringing
- Thermal stabilization time < 1 s (nominal value achieved within 10 ppm of steady state value)
- Current noise: 0.010 μ V_{RMS}/V of applied voltage (< 40 dB)
- Voltage coefficient: < 0.1 ppm/V
- Non inductive: < 0.08 μH
- Non hot spot design
- Terminal finish: gold plated (lead (Pb)-free)
- For better performances please contact us
- \bullet Available with Z-Foil technology for improved TCR to 0.2 ppm/ $^{\circ}\text{C}$
- Compliant to RoHS directive 2002/95/EC



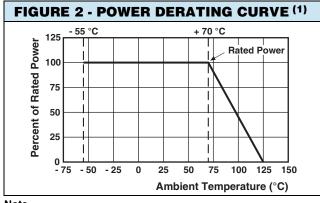
* Pb containing terminations are not RoHS compliant, exemptions may apply

SMNH

Vishay Foil Resistors

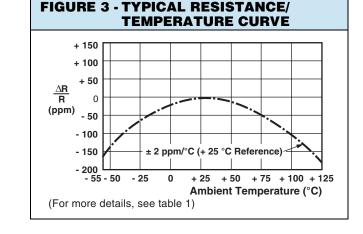


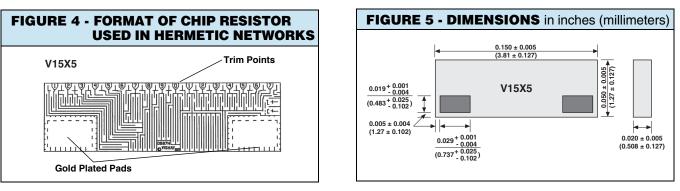
TABLE 1 - MODEL SMNH SPECIFICATIONS								
MODEL	ABSOI (- 55 °C to + 12	TOLERANCE		TCR TRACKING (max.)				
_	RESISTANCE VALUES	TYPICAL + MAX. SPREAD	ABSOLUTE	MATCH	SAME VALUES	DIFFERENT VALUES		
	500 Ω to 33 k Ω	± 2 ± 2.5 ppm/°C -	± 0.005 %	0.005 %	0.5 ppm/°C	1 ppm/°C		
SMNH	100 Ω to 500 Ω		± 0.01 %	0.005 %	2 ppm/°C	3 ppm/°C		
	10 Ω to 100 Ω	± 2 ± 3.5 ppm/°C	± 0.02 %	0.01 %	4 ppm/°C	5 ppm/°C		
	5 Ω to 10 Ω	± 2 ± 3.5 ppm/ C	± 0.05 %	0.02 %	4 ppin/ C	5 ppm/ C		

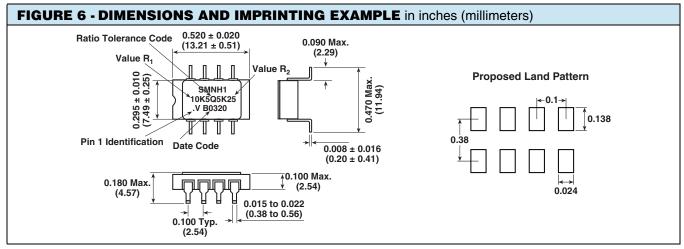


Note

⁽¹⁾ Each resistor element 0.1 W at + 70 °C (0.4 W per package)









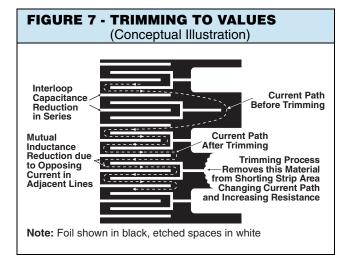


TABLE 2 - MECI	HANICAL SPECIFICATIONS
Resistive Element	High precision Bulk Metal Foil chips
Body	Ceramic package: 94 % alumina (Al ₂ O ₃)
Lid	Gold plated kovar
Terminals	Alloy 42 (iron nickel) with 100 μ" gold plating (MIL-STD-1276, type G-21-A)
Internal Connections	Gold wire bonding
Solderability	Per MIL-PRF-83401
Marking Resistance to Solvents	Permanency testing per MIL-PRF-83401

SPECIFICATIONS	TYPICAL LIMITS	MAXIMUM LIMITS
Thermal Shock 5 x (- 65 °C to + 125 °C) and Power Conditioning 1.5 rated power at 25 °C, 100 h	∆R = 0.003 % (30 ppm) ∆Ratio = 0.001 % (10 ppm)	0.015 % (150 ppm) 0.015 % (150 ppm)
Low Temperature Operation	$\Delta R = 0.005 \% (50 \text{ ppm})$ $\Delta Ratio = 0.005 \% (50 \text{ ppm})$	0.01 % (100 ppm) 0.01 % (100 ppm) 0.01 % (100 ppm)
Short Time Overload 6.25 rated power; 5 s	ΔR = 0.002 % (20 ppm) ΔRatio = 0.002 % (20 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Terminal Strength	∆R = 0.001 % (10 ppm) ∆Ratio = 0.001 % (10 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Resistance to Soldering Heat 260 °C, 10 s	∆R = 0.002 % (20 ppm) ∆Ratio = 0.001 % (10 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Moisture Resistance	∆R = 0.003 % (30 ppm) ∆Ratio = 0.003 % (30 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Shock 100 G, sawtooth	∆R = 0.001 % (10 ppm) ∆Ratio = 0.001 % (10 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Vibration, High Frequency	∆R = 0.001 % (10 ppm) ∆Ratio = 0.001 % (10 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Load Life 1000 h at + 70 °C; 0.1 W per resistor	∆R = 0.005 % (50 ppm) ∆Ratio = 0.005 % (50 ppm)	0.025 % (250 ppm) 0.01 % (100 ppm)
Load Life 1000 h at + 25 °C; 0.1 W per resistor	∆R = 0.002 % (20 ppm) ∆Ratio = 0.001 % (10 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
High Temperature Exposure 100 h at + 125 °C	∆R = 0.005 % (50 ppm) ∆Ratio = 0.005 % (50 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Low Temperature Storage 24 h at - 65 °C	∆R = 0.002 % (20 ppm) ∆Ratio = 0.002 % (20 ppm)	0.01 % (100 ppm) 0.01 % (100 ppm)
Shelf Life Stability (6 years)	ΔR = 0.0002 % (2 ppm) ΔRatio = 0.0001 % (1 ppm)	
Insulation Resistance 100 V (DC)	$> 10^4 M\Omega$	
DWV Atm. Pressure	200 V	
Weight	0.95 g	

Notes

 ΔR's are not cumulative. For purposes of determining reliability calculations, consider the characteristics shown as figures of merit and allow no more than ± 0.05 ΔR lifetime. Allow proportionately less if the severity of anticipated environmental stresses are small compared to the tests as defined in MIL-PRF-83401.

• Post manufacturing operation (PMO) has the effect of minimizing ΔR 's. Consult Vishay applications engineering for details.

• ΔRatio refers to the change in ratio between resistors within the network package from before, to after, the specified test.



QUALITY INSPECTION AND TESTING

Network performance is established during the engineering design phase and is dependent on the materials of construction. Most characteristics are inherent in the Bulk Metal Foil technology and provide the high order of performance displayed throughout this catalog. Stability and drift levels can be improved beyond those shown in the catalog. Applications engineering is available to recommend screen testing beyond the standard outgoing inspection when catalog limits are insufficient. The chart below shows the standard outgoing testing and the additional user specified screen tests that may be appropriate for a particular application.

TESTING OF COMMERCIAL PRODUCT - VISHAY NETWORKS

Our standard outgoing testing consists of:

- 1. DC resistance test 100 %
 - 1.1 Conformity to value
 - 1.2 Conformity to tolerance
- 2. Visual and Mechanical 100 %
 - 2. 1 Conformity to physical size
 - 2.2 Cleanliness of leads
 - 2.3 Conformity of printing
- 3. Ship check (sample plan)
 - 3. 1 Conformity of packaging
 - 3.2 Conformity of count

ADDITIONAL TESTING TO MIL SPEC

Group A testing to MIL-PRF-83401 imposes the following:

1. Thermal shock 100 %

5X from - 65 °C to + 125 °C

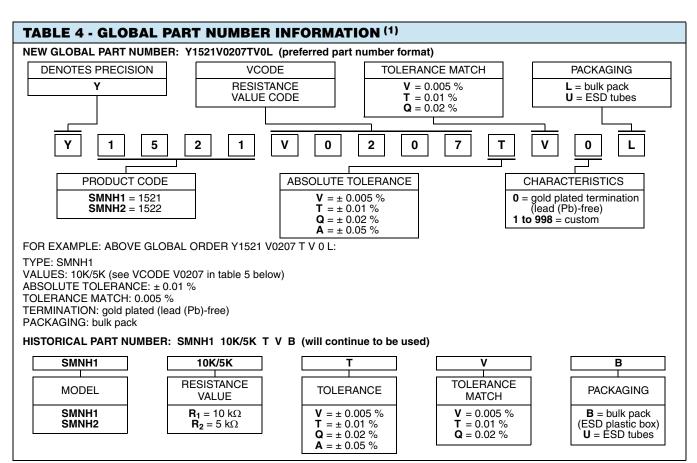
- 2. Power conditioning 100 %
 - 2. 1 100 h at 25 °C, 1.5 x rated power
 - 2. 2 ΔR and $\Delta Ratio$ calculation
- 3. Visual and mechanical after the above tests (sample plan)
 - 3.1 Conformity to physical size
 - 3.2 Workmanship
 - 3.3 Damage due to the above tests
- 4. 10 % PDA or one piece whichever is greater
- 5. Solderability (sample plan)

Group B sample testing to MIL-PRF-83401 imposes the following:

- 1. Temperature coefficient of resistance (sample plan)
- 2. Resistance to solvents (sample plan)



SMNH



Note

⁽¹⁾ Application engineering release: for non-standard requests, please contact application engineering

VCODES	R₁/R₂ RATIO	R ₁	R ₂	VCODES	R₁/R₂ RATIO	R ₁	R_2
V0201	100	10K	100R	V0189	2.5	1K	400R
V0202	50	10K	200R	V0185	2.0	500R	200R
V0197		5K	100R	V0207		10K	5K
V0203	25	10K	400R	V0175	2	2K	1K
V0198		5K	200R	V0190		1K	500R
V0204	20	10K	500R	V0182		400R	200R
V0193		2K	100R	V0179		200R	100R
V0205	10	10K	1K	V0186	1.25	500R	400R
V0194		2K	200R	V0178		100R	100R
V0187		1K	100R	V0180		200R	200R
V0200	5	5K	1K	V0183		400R	400R
V0195		2K	400R	V0023	1	500R	500R
V0188		1K	200R	V0191		1K	1K
V0184		500R	100R	V0176		2K	2K
V0196	4	2K	500R	V0019		5K	5K
V0181		400R	100R	V0008		10K	10K

Note

Other values available upon request



Vishay Precision Group

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